

IN THE CLAIMS

Please amend the claims as follows:

1. - 11. (Canceled)

12. (Currently Amended) A logic unit comprising:
a first logic unit connected to a first supply voltage;
a second logic unit connected to a second supply voltage; and
a logic circuit connecting the first logic unit to the second logic unit, the logic circuit
including at least one transistor having a threshold voltage greater than or about equal to the
difference between the second supply voltage and the first supply voltage and the at least one
transistor connected to the second supply voltage, The logic unit of claim 11, wherein the logic
circuit comprises an AND circuit.

13. (Currently Amended) A logic unit comprising:
a first logic unit connected to a first supply voltage;
a second logic unit connected to a second supply voltage; and
a logic circuit connecting the first logic unit to the second logic unit, the logic circuit
including at least one transistor having a threshold voltage greater than or about equal to the
difference between the second supply voltage and the first supply voltage and the at least one
transistor connected to the second supply voltage, The logic unit of claim 11, wherein the logic
circuit comprises a NAND circuit.

14. (Currently Amended) A logic unit comprising:
a first logic unit connected to a first supply voltage;
a second logic unit connected to a second supply voltage; and
a logic circuit connecting the first logic unit to the second logic unit, the logic circuit
including at least one transistor having a threshold voltage greater than or about equal to the
difference between the second supply voltage and the first supply voltage and the at least one
transistor connected to the second supply voltage, The logic unit of claim 11, wherein the logic
circuit comprises an OR circuit.

15. (Currently Amended) A logic unit comprising:
a first logic unit connected to a first supply voltage;
a second logic unit connected to a second supply voltage; and
a logic circuit connecting the first logic unit to the second logic unit, the logic circuit
including at least one transistor having a threshold voltage greater than or about equal to the
difference between the second supply voltage and the first supply voltage and the at least one
transistor connected to the second supply voltage, The logic unit of claim 11, wherein the logic
circuit comprises a NOR circuit.

16. (Currently Amended) A logic unit comprising:
a first logic unit connected to a first supply voltage;
a second logic unit connected to a second supply voltage; and
a logic circuit connecting the first logic unit to the second logic unit, the logic circuit
including at least one transistor having a threshold voltage greater than or about equal to the
difference between the second supply voltage and the first supply voltage and the at least one
transistor connected to the second supply voltage, The logic unit of claim 11, wherein the logic
circuit comprises an XOR circuit.

17. (Canceled)

18. (Currently Amended) A logic unit comprising:
a first logic unit connected to a first supply voltage;
a second logic unit connected to a second supply voltage; and
a logic circuit connecting the first logic unit to the second logic unit, the logic circuit
including at least one transistor having a threshold voltage greater than or about equal to the
difference between the second supply voltage and the first supply voltage and the at least one
transistor connected to the second supply voltage, wherein the second logic unit comprises a
clock distribution circuit and ~~The logic unit of claim 17, wherein the logic circuit comprises a~~
NAND circuit.

19. (Currently Amended) A logic unit comprising:
a first logic unit connected to a first supply voltage;
a second logic unit connected to a second supply voltage; and
a logic circuit connecting the first logic unit to the second logic unit, the logic circuit
including at least one transistor having a threshold voltage greater than or about equal to the
difference between the second supply voltage and the first supply voltage and the at least one
transistor connected to the second supply voltage, wherein the second logic unit comprises a
clock distribution circuit and ~~The logic unit of claim 17, wherein the logic circuit comprises a~~
NOR circuit.

20. (Canceled)

21. (Currently Amended) A logic unit comprising:
a first logic unit connected to a first supply voltage;
a second logic unit connected to a second supply voltage; and
a logic circuit connecting the first logic unit to the second logic unit, the logic circuit
including at least one transistor having a threshold voltage greater than or about equal to the
difference between the second supply voltage and the first supply voltage and the at least one
transistor connected to the second supply voltage, wherein the second logic unit comprises an
arithmetic unit and ~~The logic unit of claim 20, wherein the logic circuit comprises an OR circuit.~~

22. (Currently Amended) A logic unit comprising:
a first logic unit connected to a first supply voltage;
a second logic unit connected to a second supply voltage; and
a logic circuit connecting the first logic unit to the second logic unit, the logic circuit
including at least one transistor having a threshold voltage greater than or about equal to the
difference between the second supply voltage and the first supply voltage and the at least one
transistor connected to the second supply voltage, wherein the second logic unit comprises an
arithmetic unit and ~~The logic unit of claim 20, wherein the logic circuit comprises an XOR~~
circuit.

23. - 25. (Canceled)